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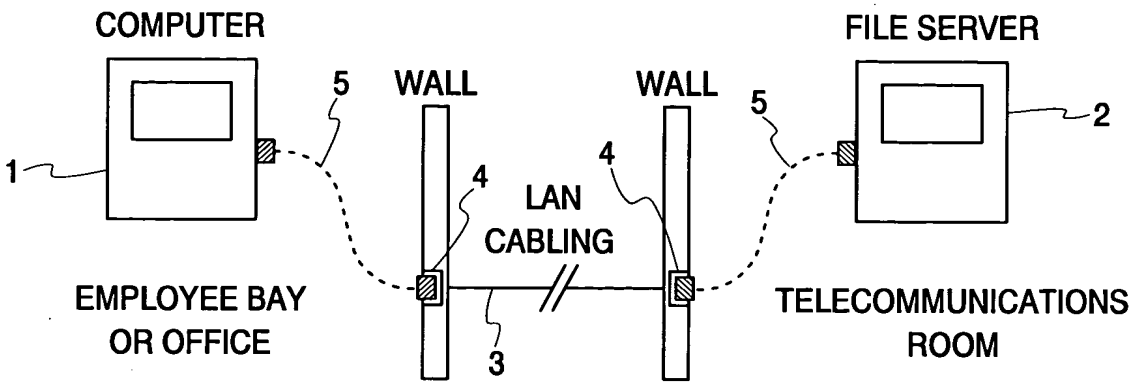

Fig. 1

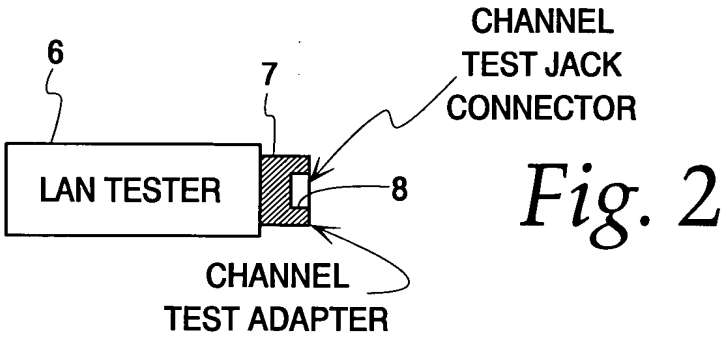

Fig. 2

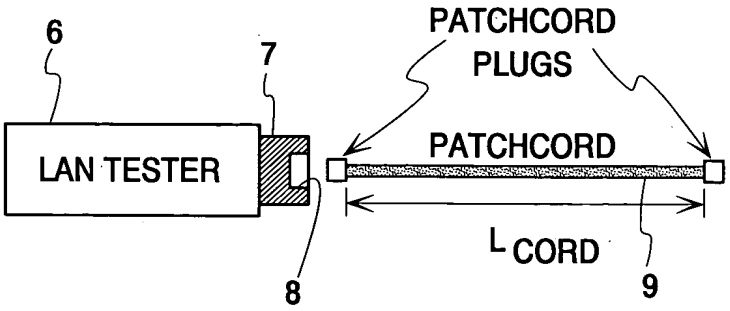

Fig. 3

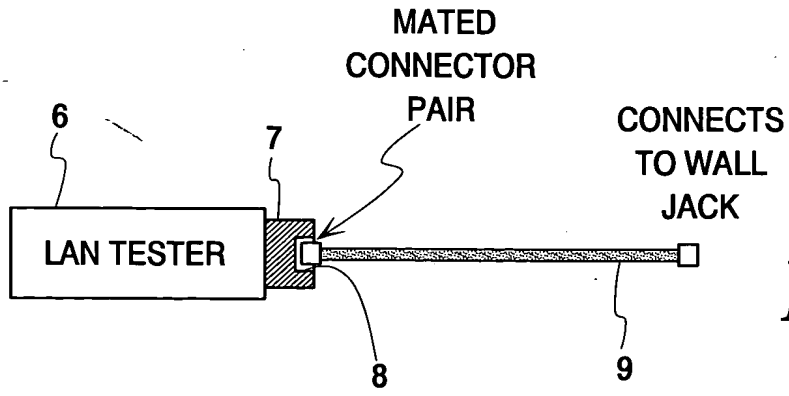

Fig. 4

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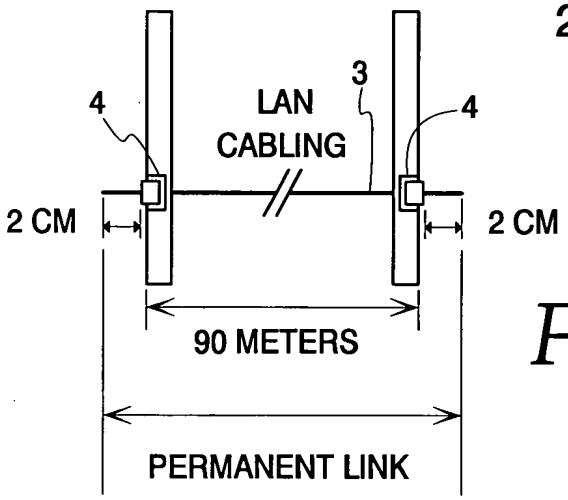

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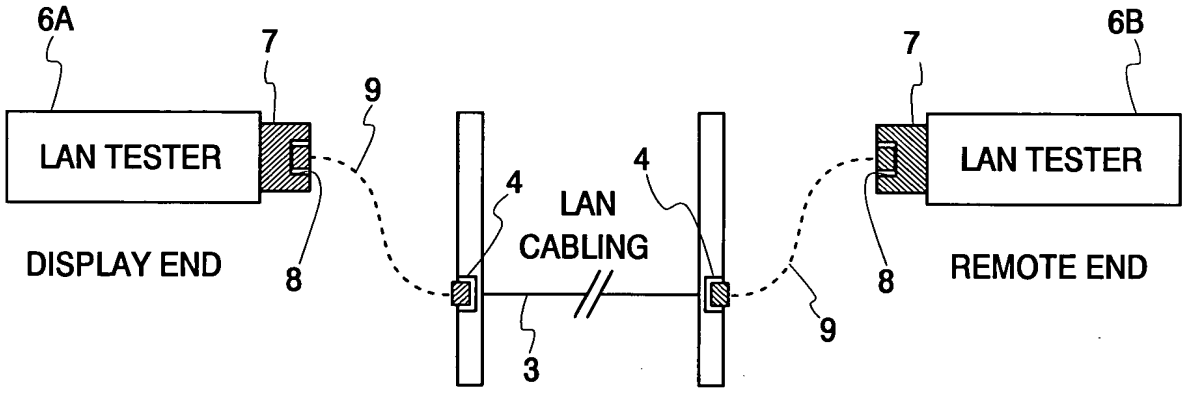

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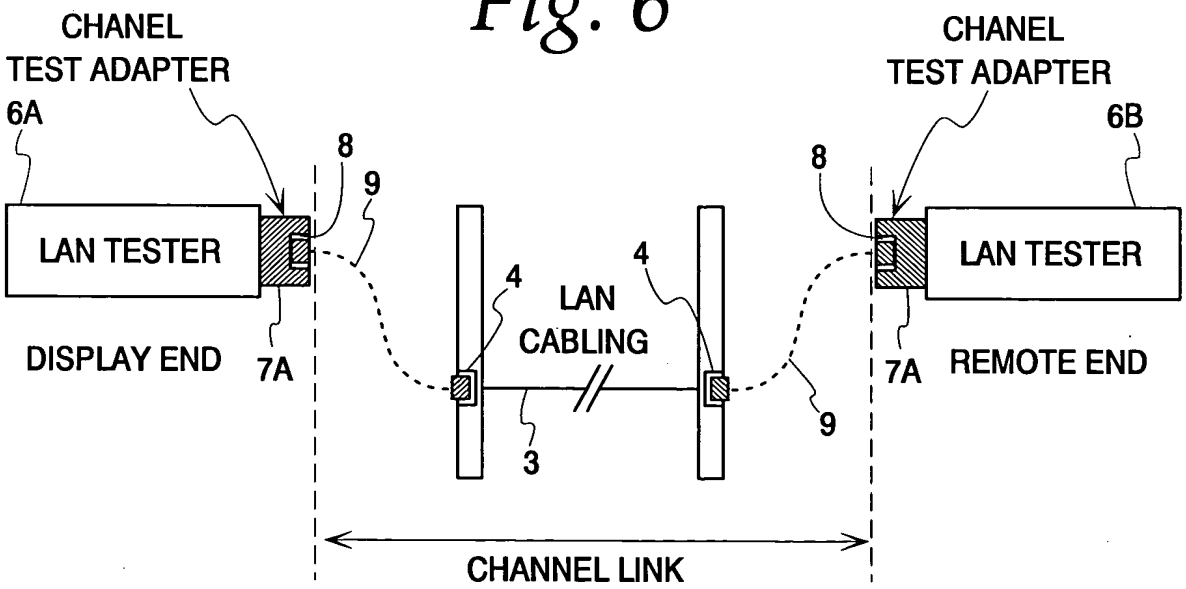

Fig. 7

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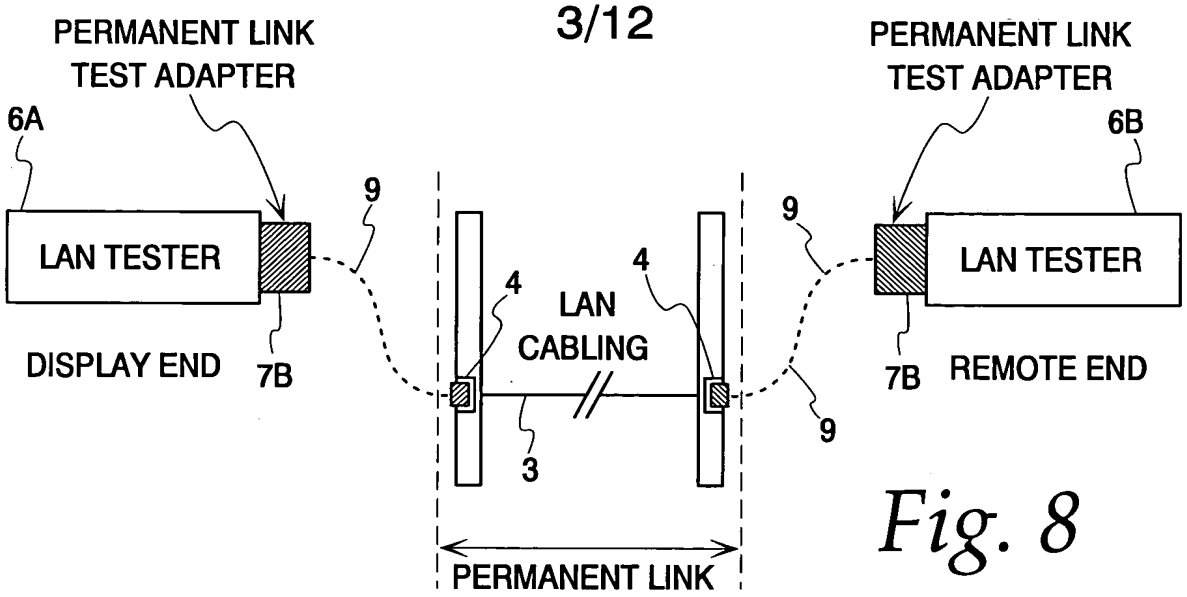

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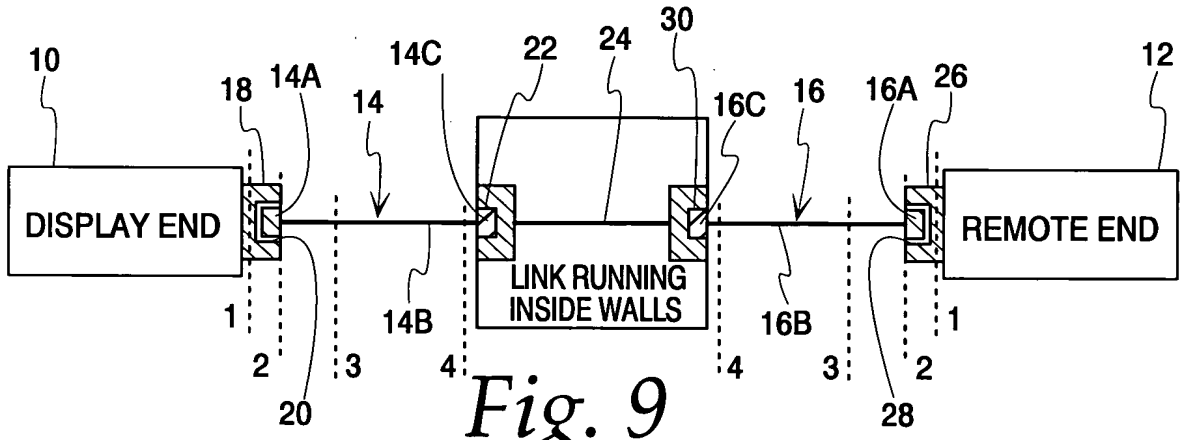

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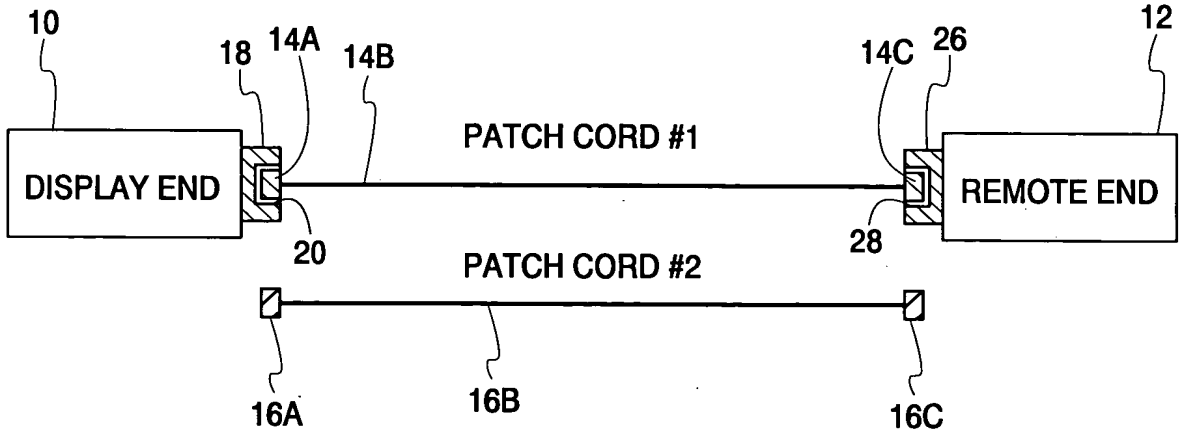

Fig. 10

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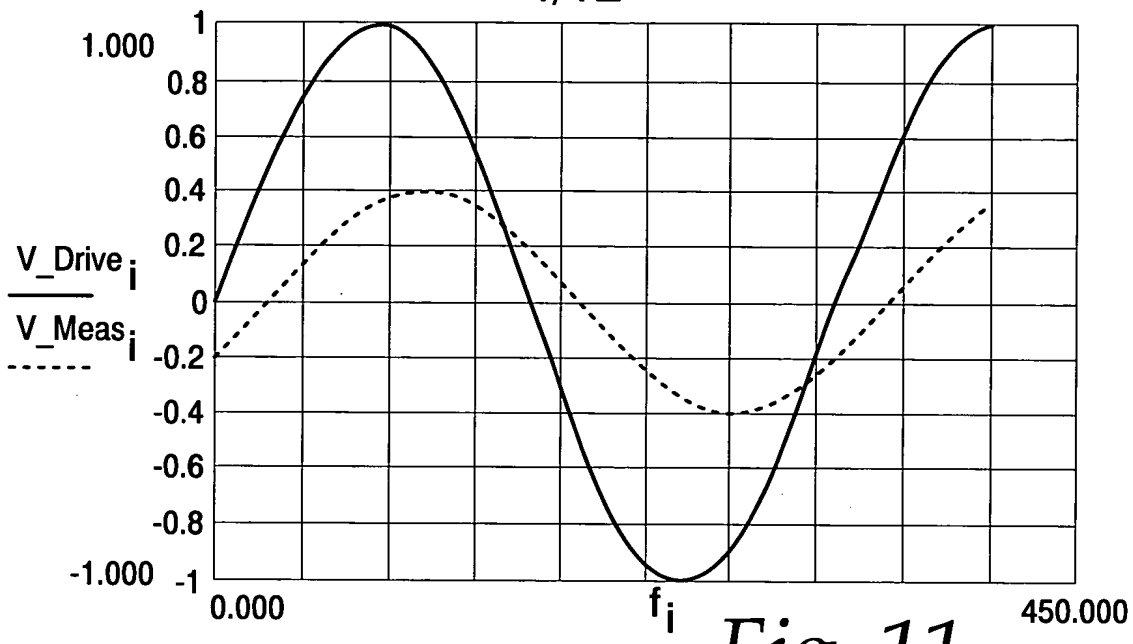

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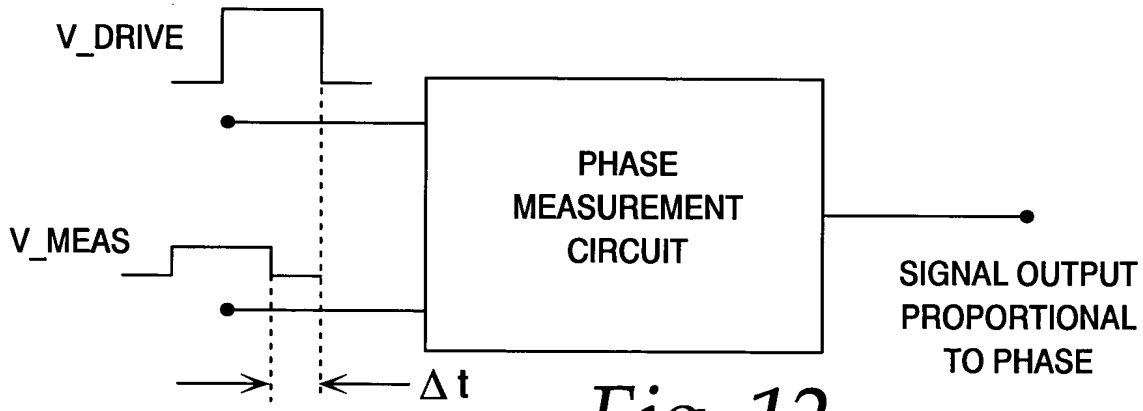

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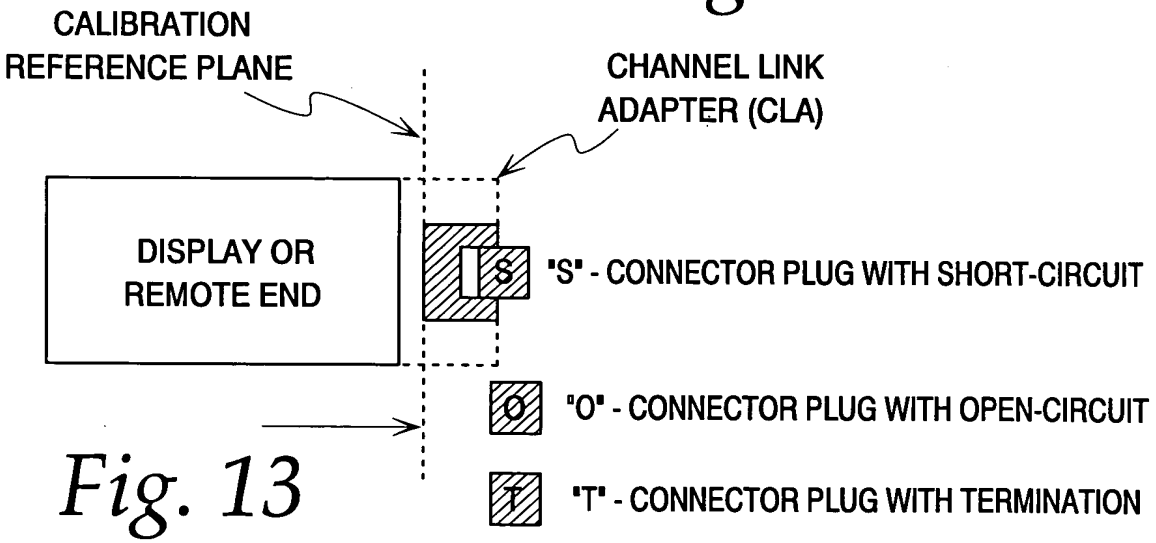

Fig. 13

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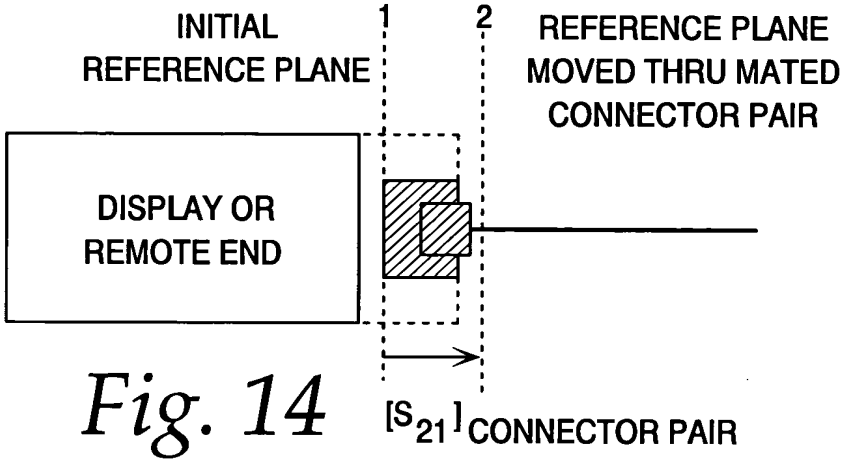

Fig. 14

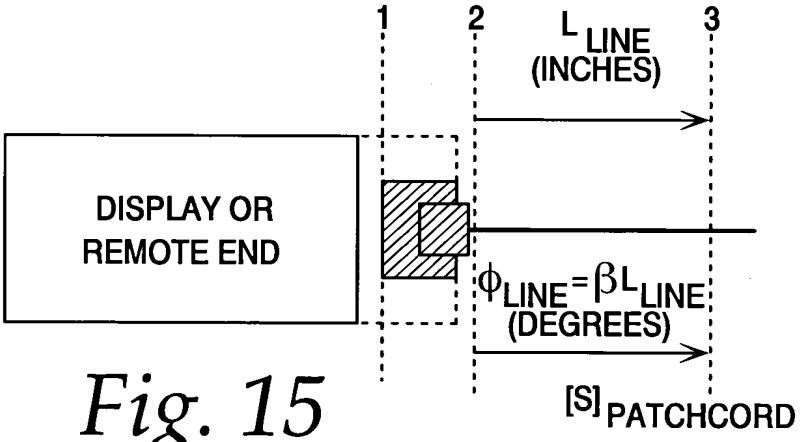

Fig. 15

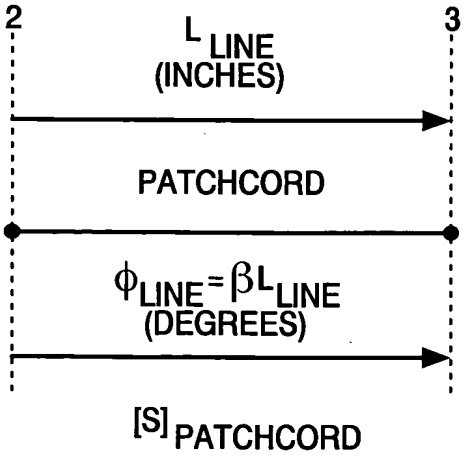

Fig. 16

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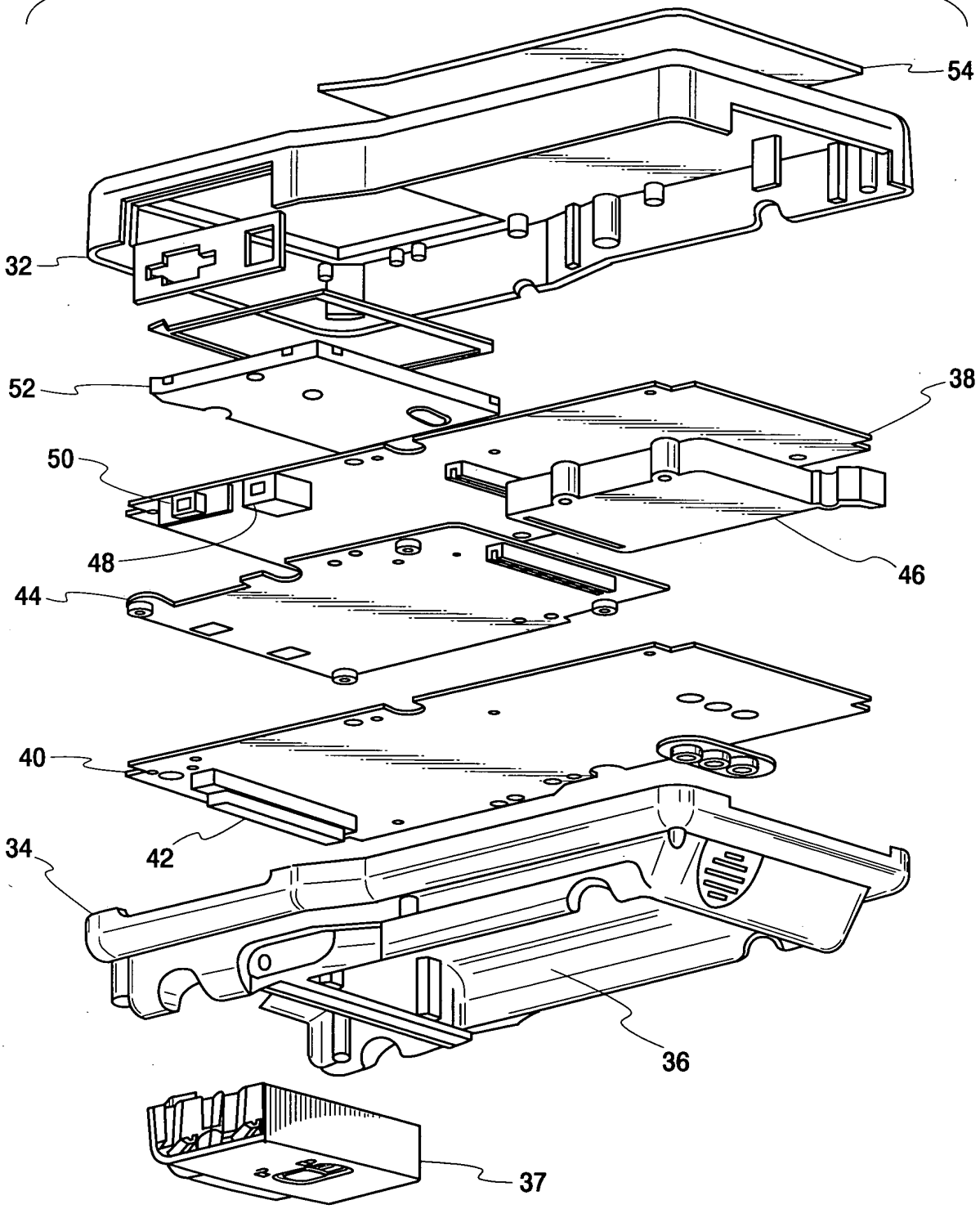
Fig. 17



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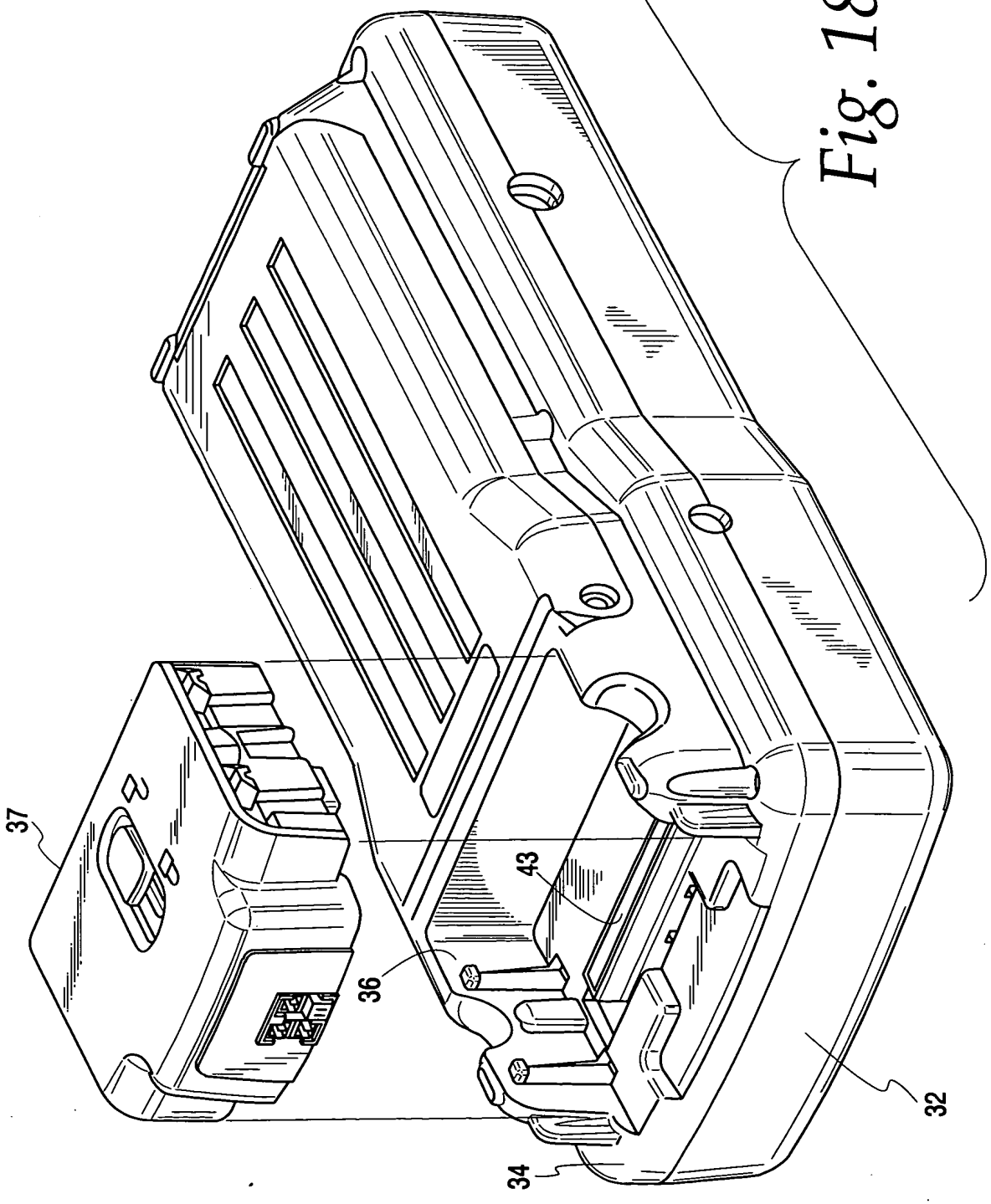

Fig. 18

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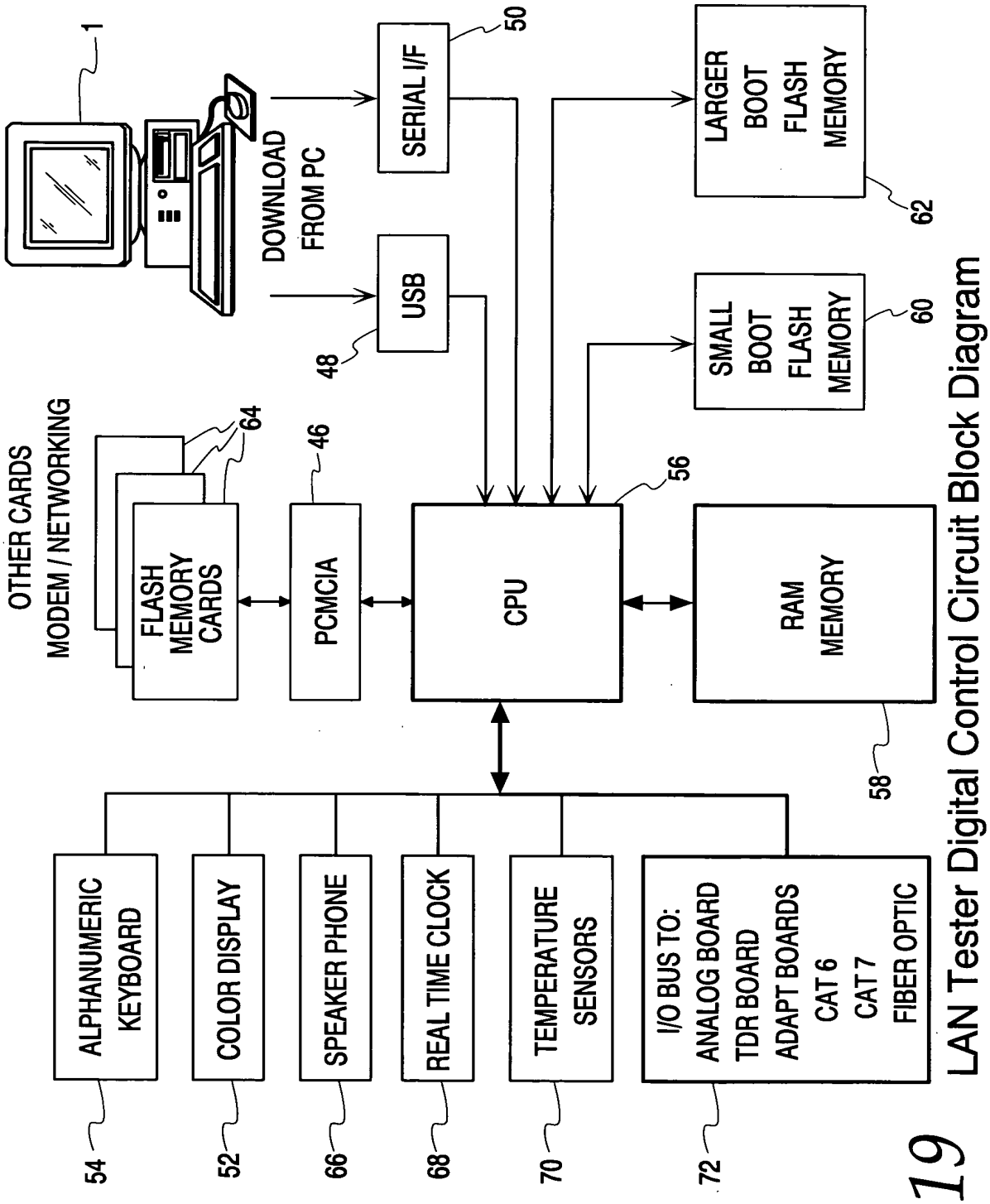

Fig. 19 LAN Tester Digital Control Circuit Block Diagram

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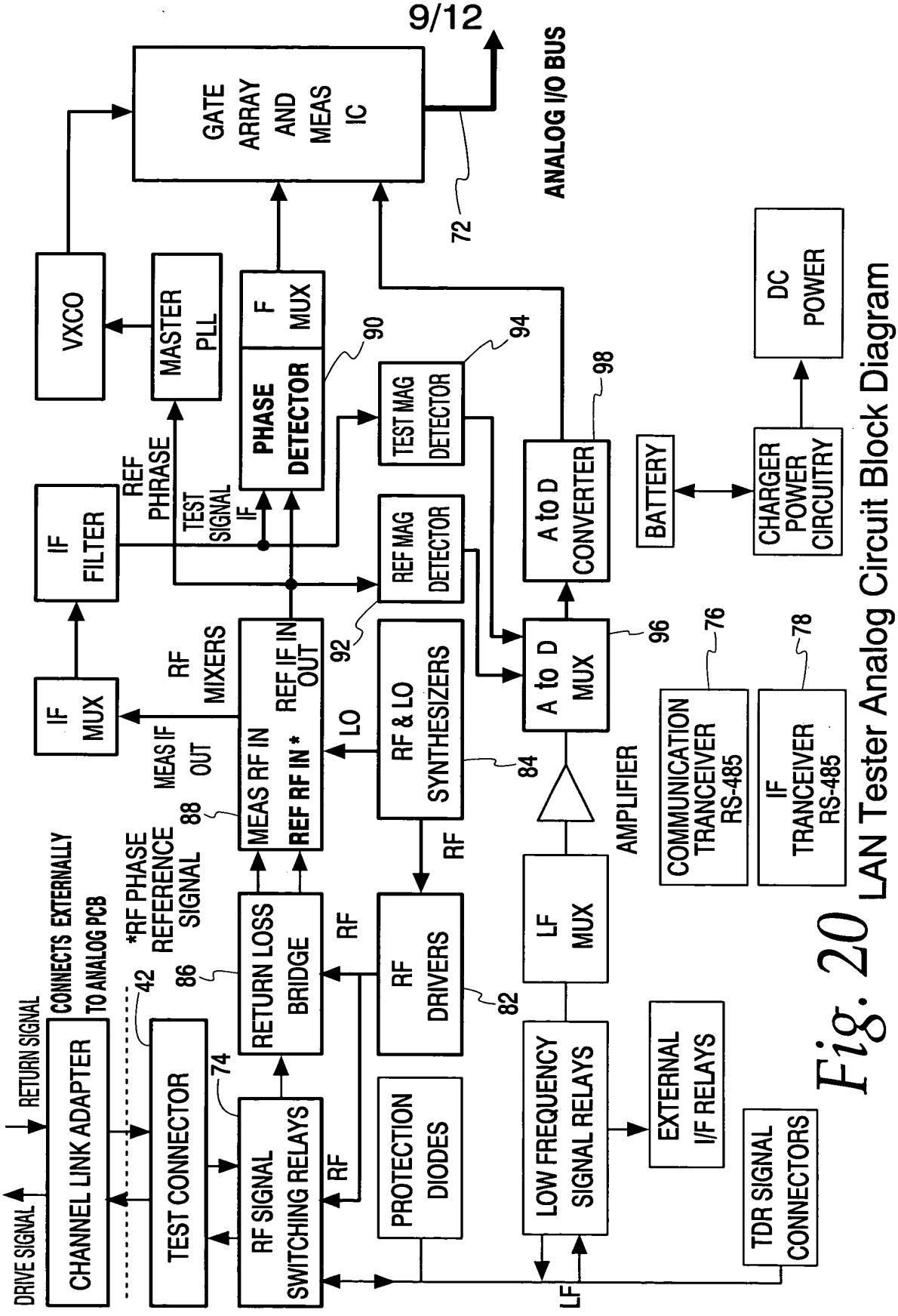

Fig. 20 LAN Tester Analog Circuit Block Diagram

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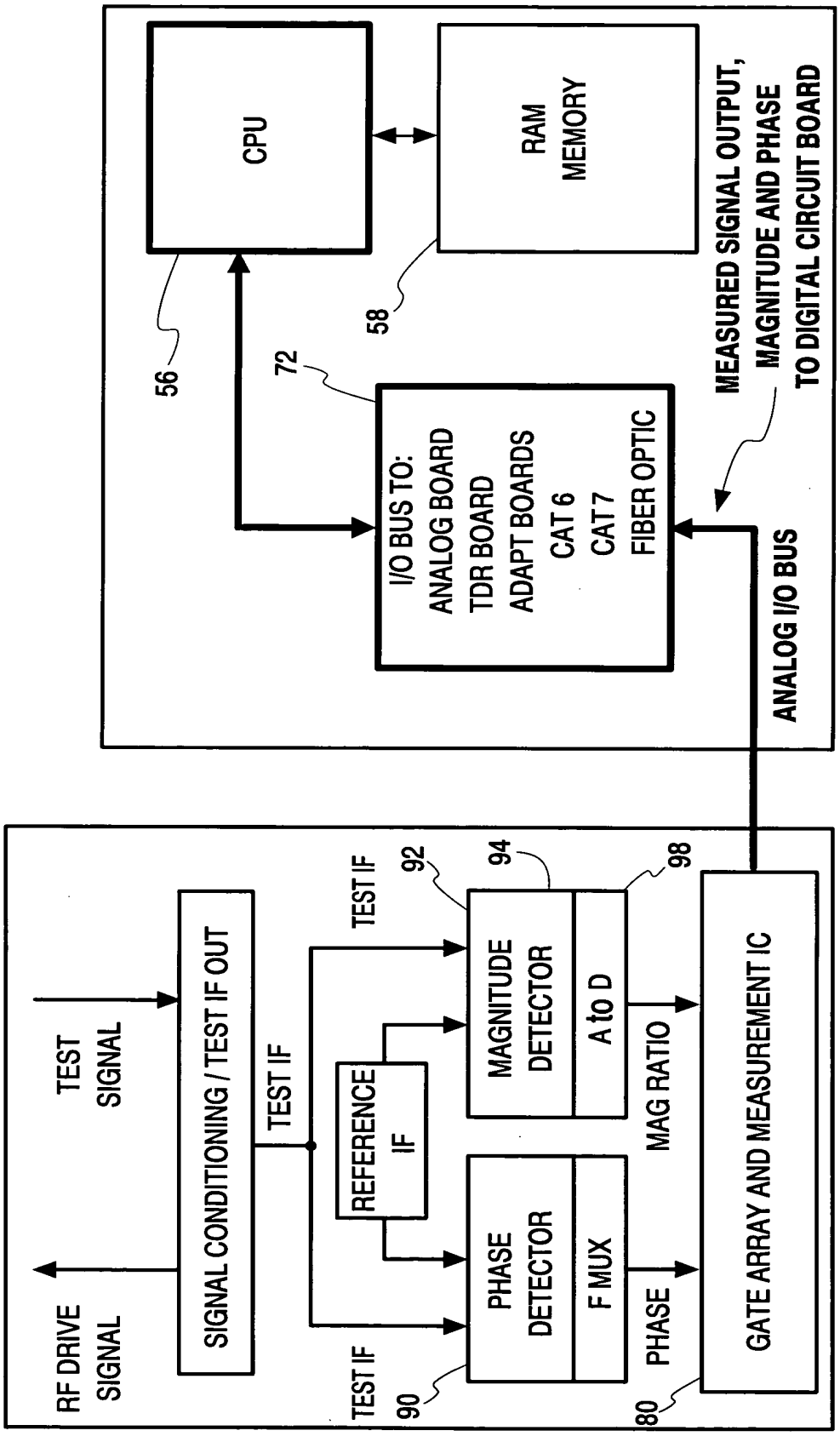

Fig. 21 LAN Tester Detailed Phase Measurement Block Diagram

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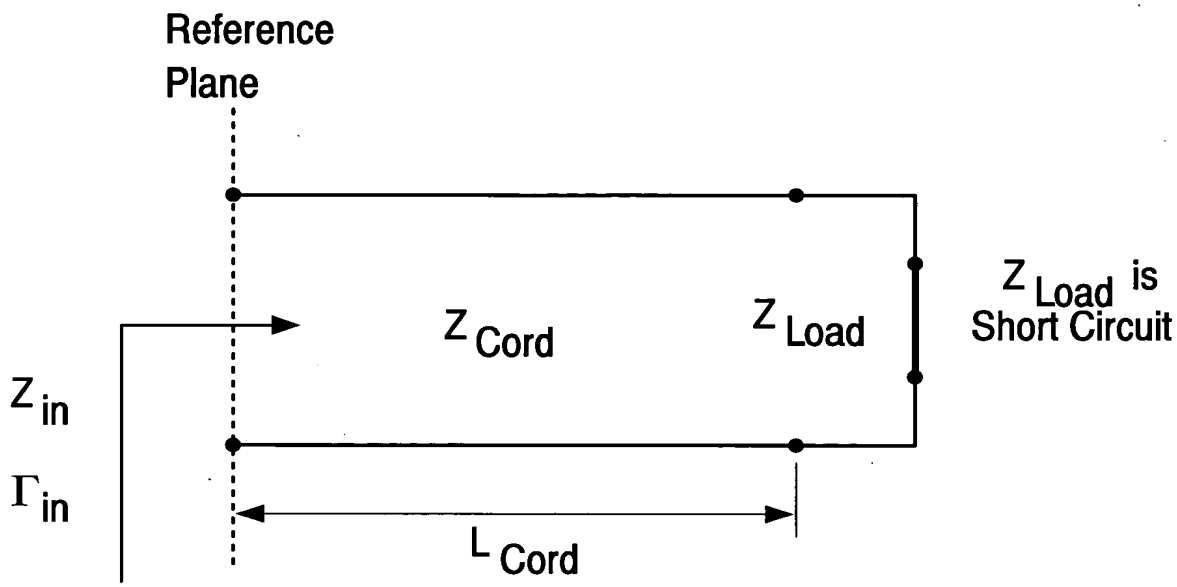

Fig. 22 Patch Cord with Short-Circuit Load

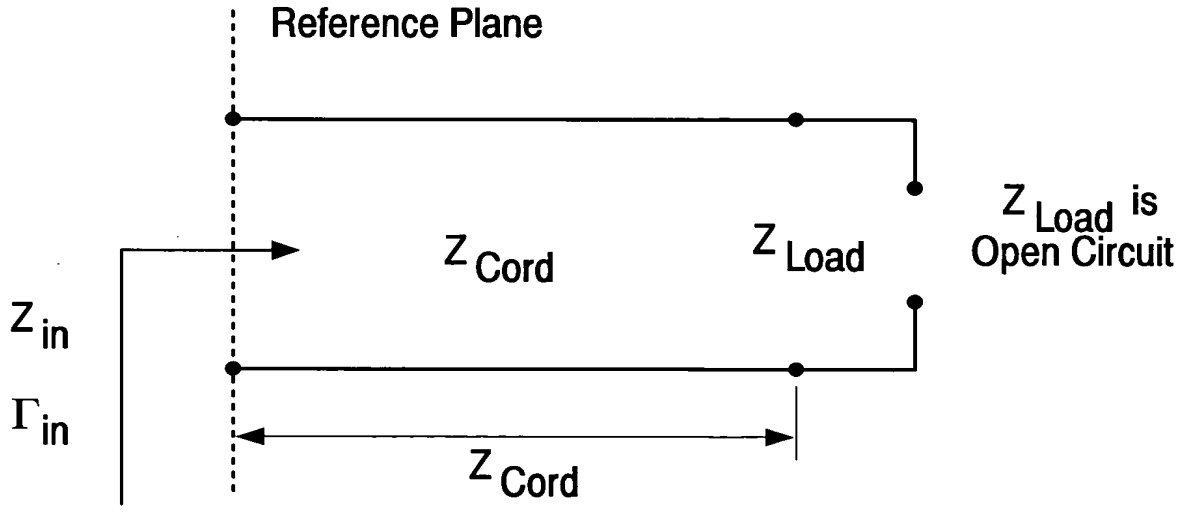

Fig. 23 Patch Cord with Open-Circuit Load

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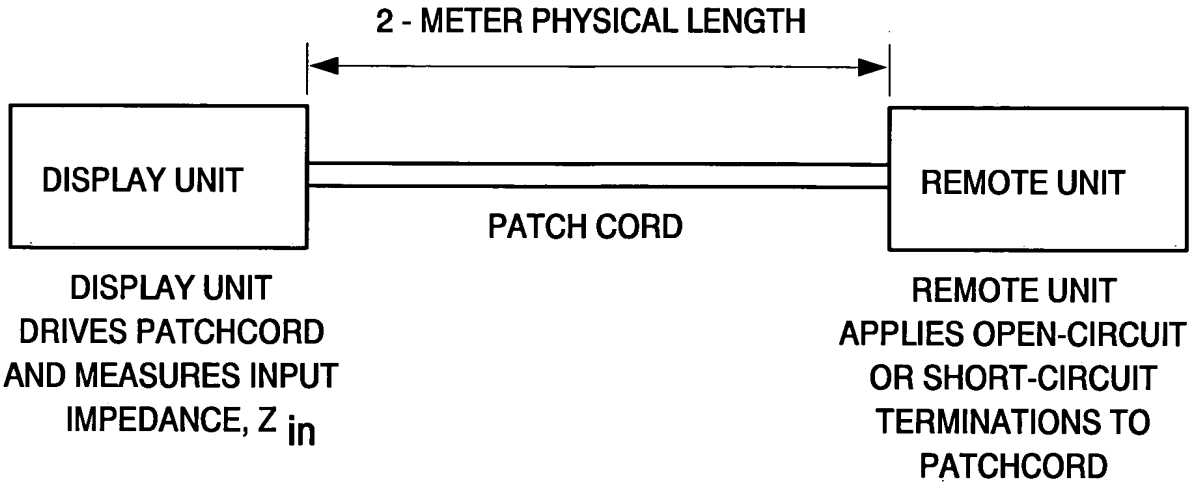

Fig. 24 PATCHCORD PHYSICAL LENGTH MEASUREMENT TEST CONFIGURATION

Fig. 25